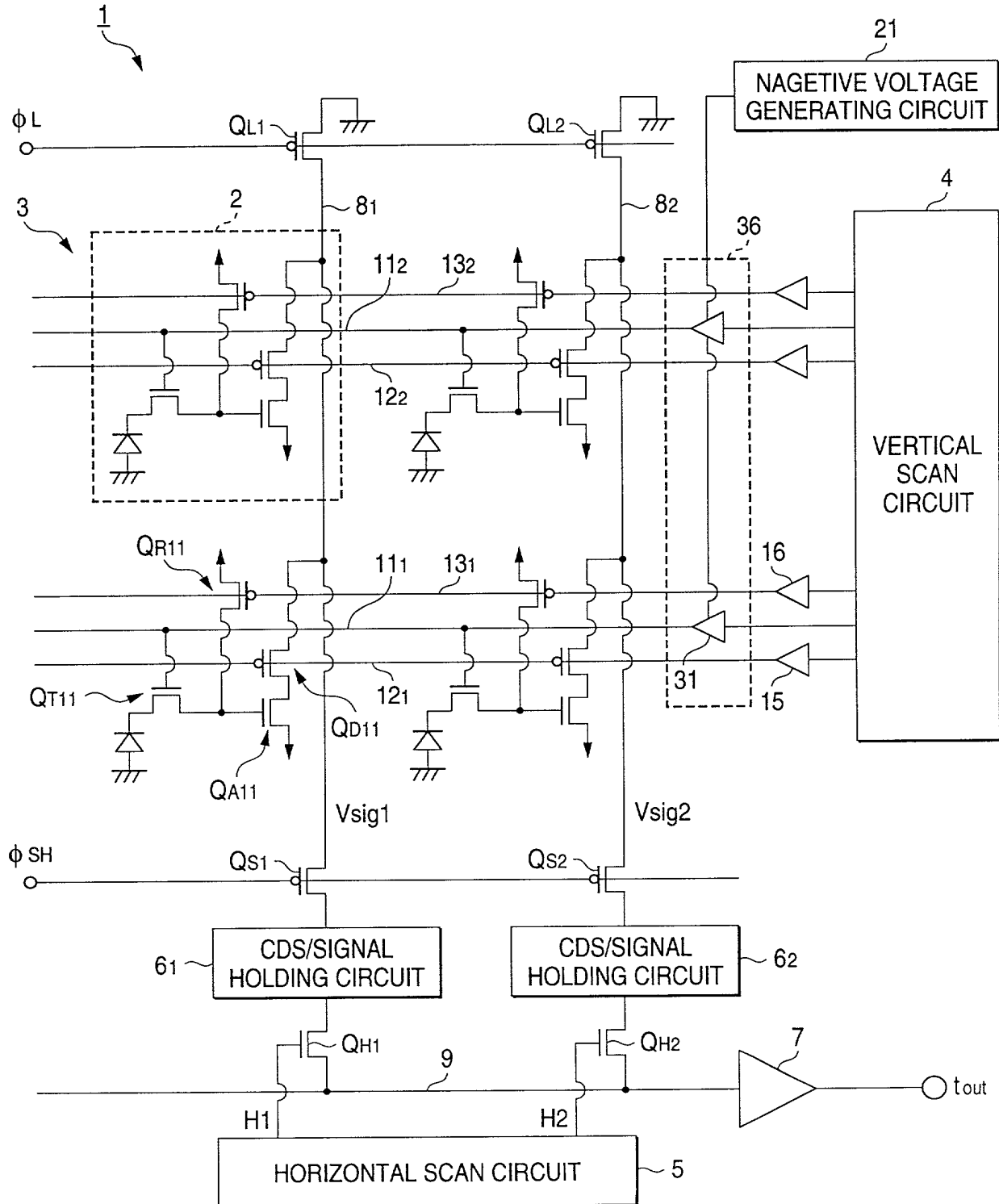
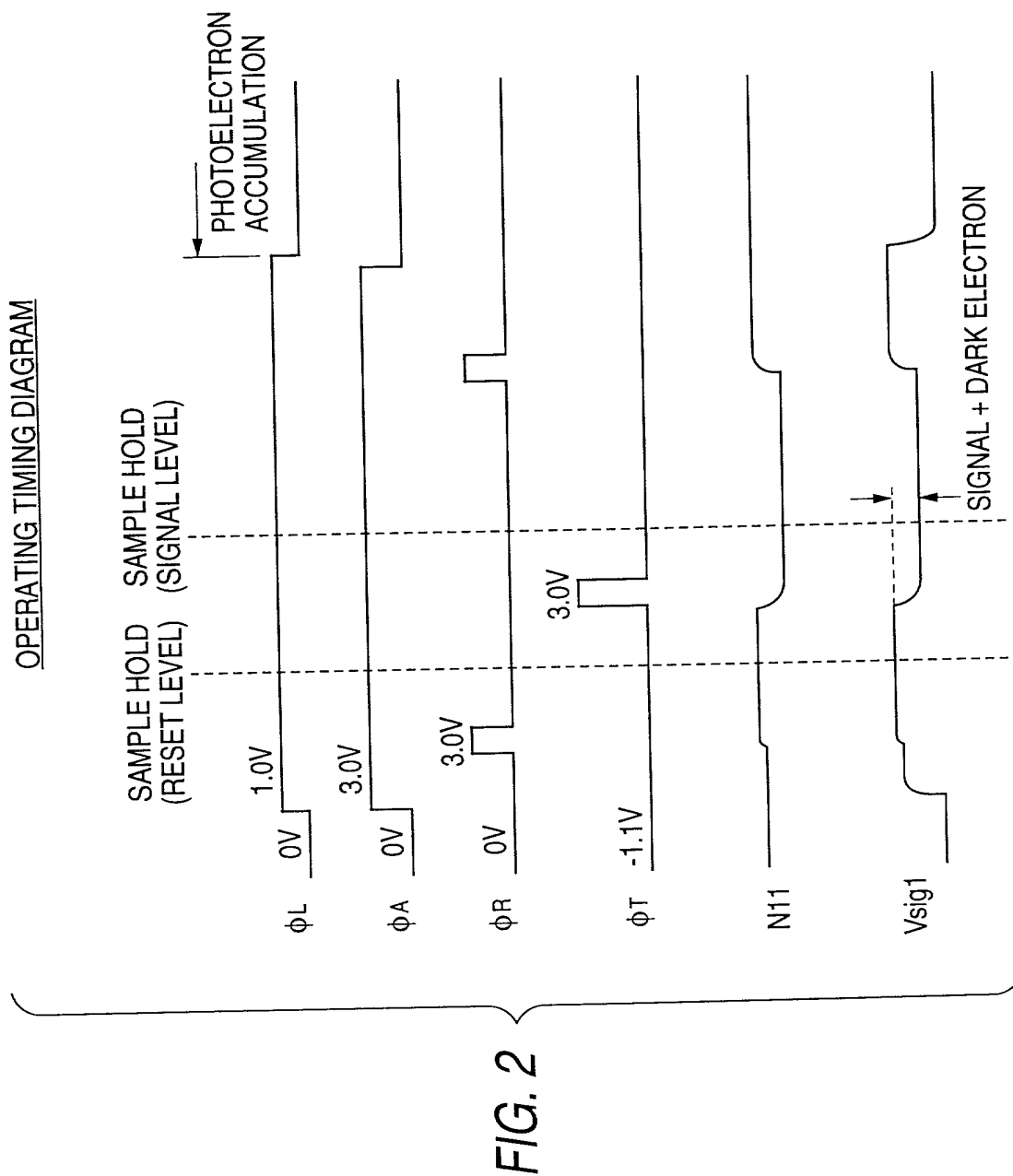


FIG. 1

DIAGRAM OF CONSTRUCTION OF EMBODIMENT





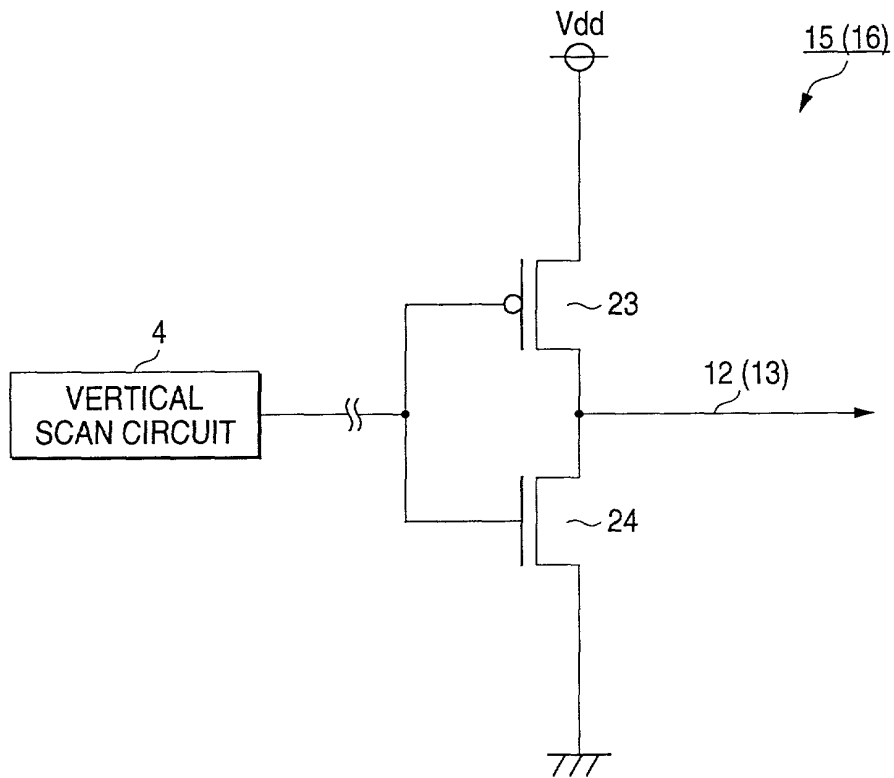
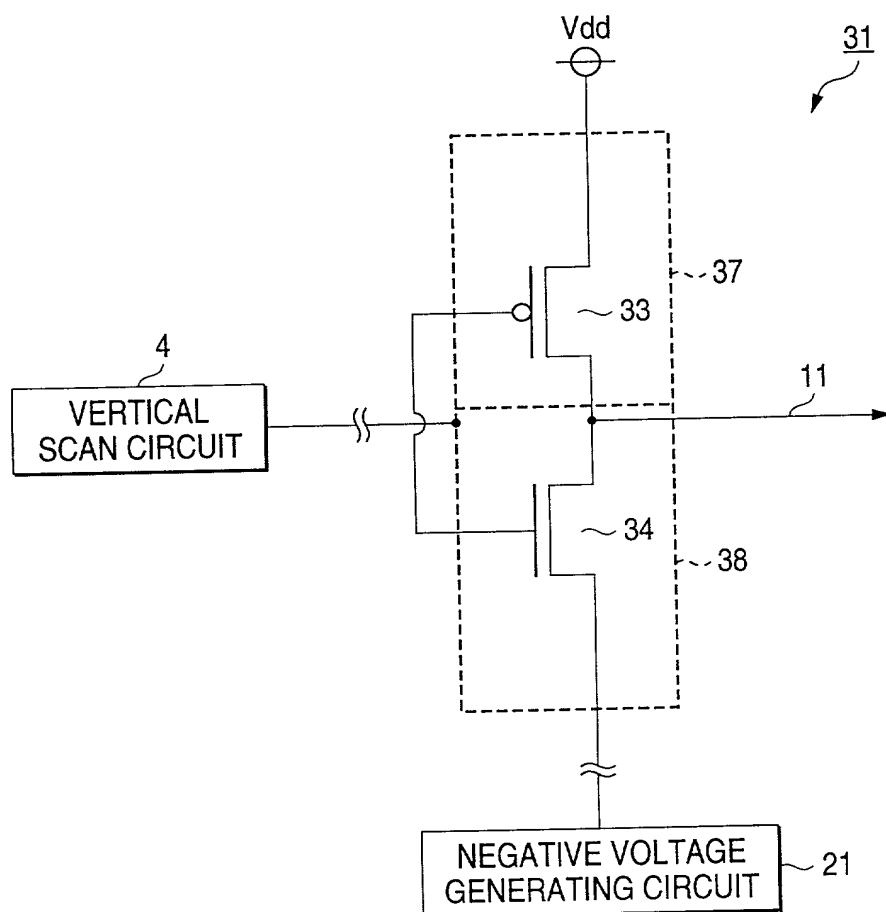
*FIG. 3*EXAMPLE OF BUFFER CIRCUIT (15, 16)

FIG. 4

EXAMPLE OF BUFFER CIRCUIT (31)



CROSS-SECTIONAL VIEW OF PHOTODIODE AND
TRANSFER TRANSISTOR OF FIRST EMBODIMENT

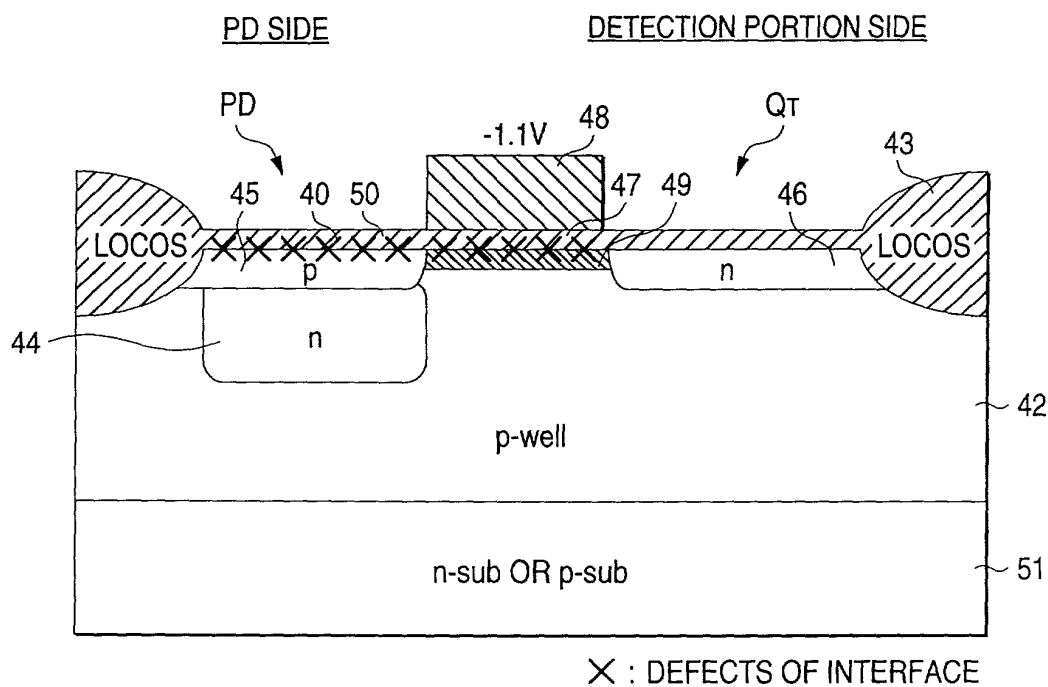


FIG. 6

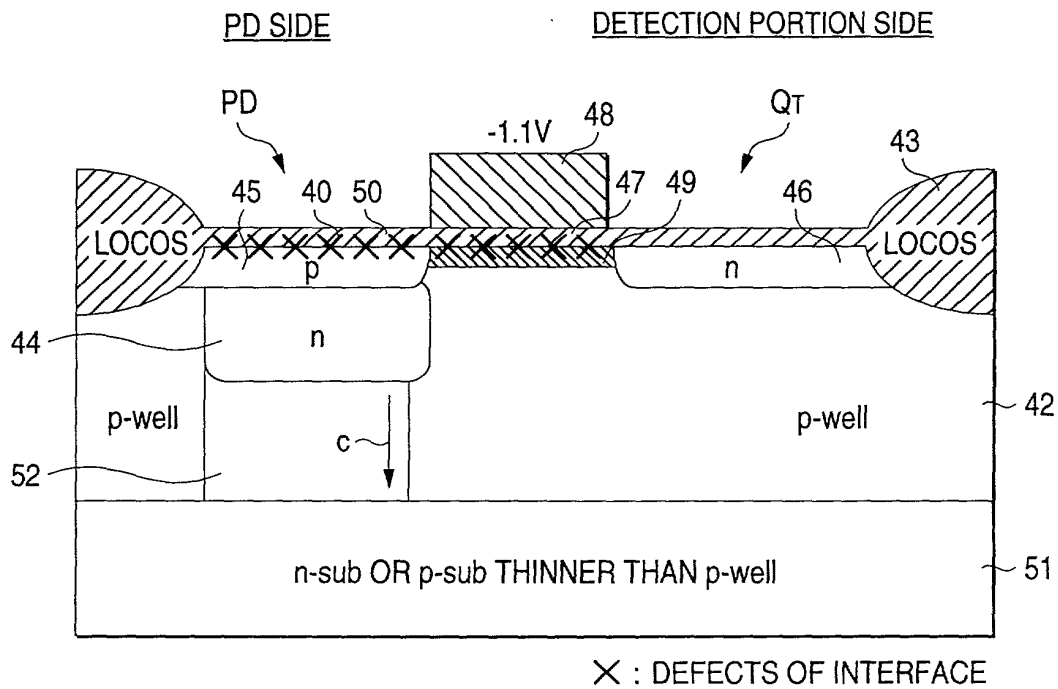
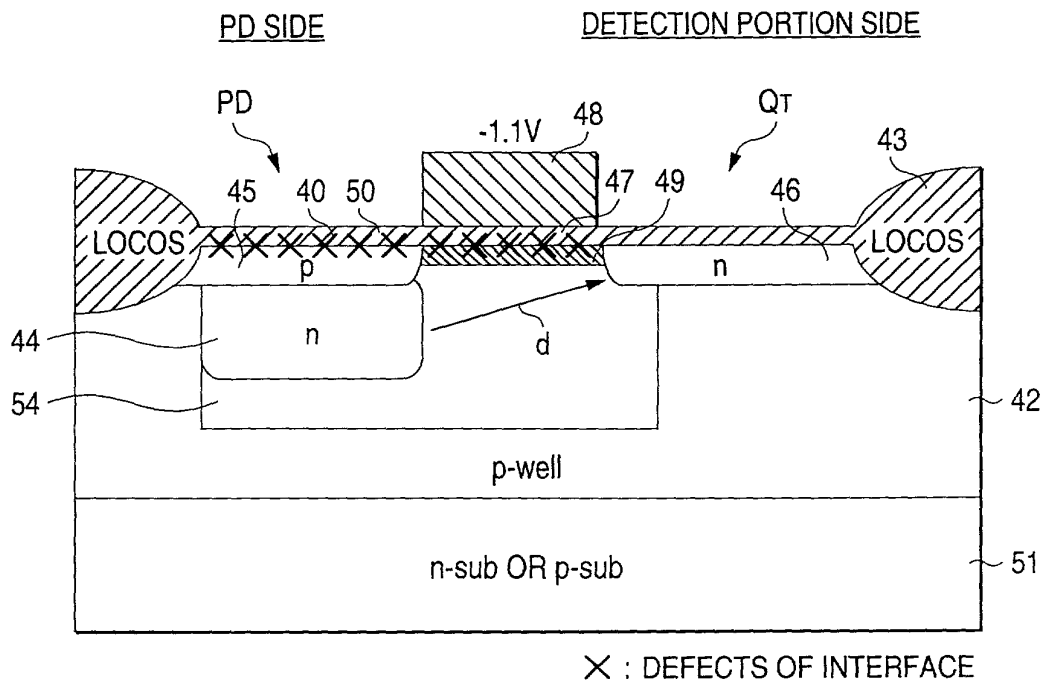
CROSS-SECTIONAL VIEW OF PHOTODIODE AND
TRANSFER TRANSISTOR OF SECOND EMBODIMENT

FIG. 7

CROSS-SECTIONAL VIEW OF PHOTODIODE AND
TRANSFER TRANSISTOR OF THIRD EMBODIMENT

CROSS-SECTIONAL VIEW OF PHOTODIODE AND TRANSFER TRANSISTOR OF FOURTH EMBODIMENT

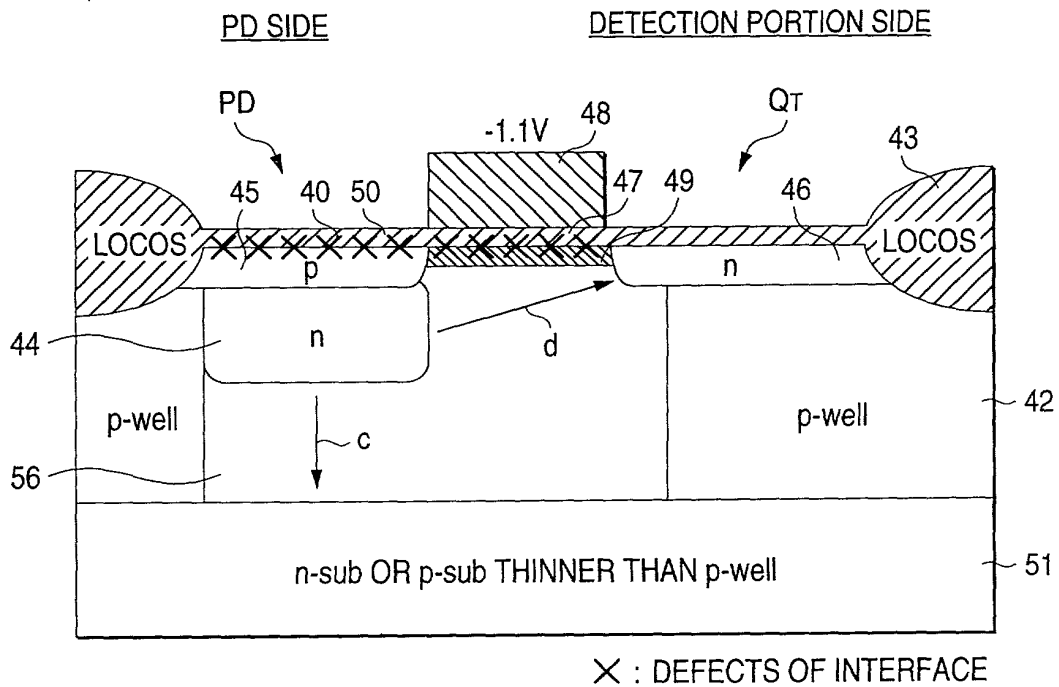


FIG. 9

TRANSFER GATE NEGATIVE VOLTAGE - DARK CURRENT CHARACTERISTIC

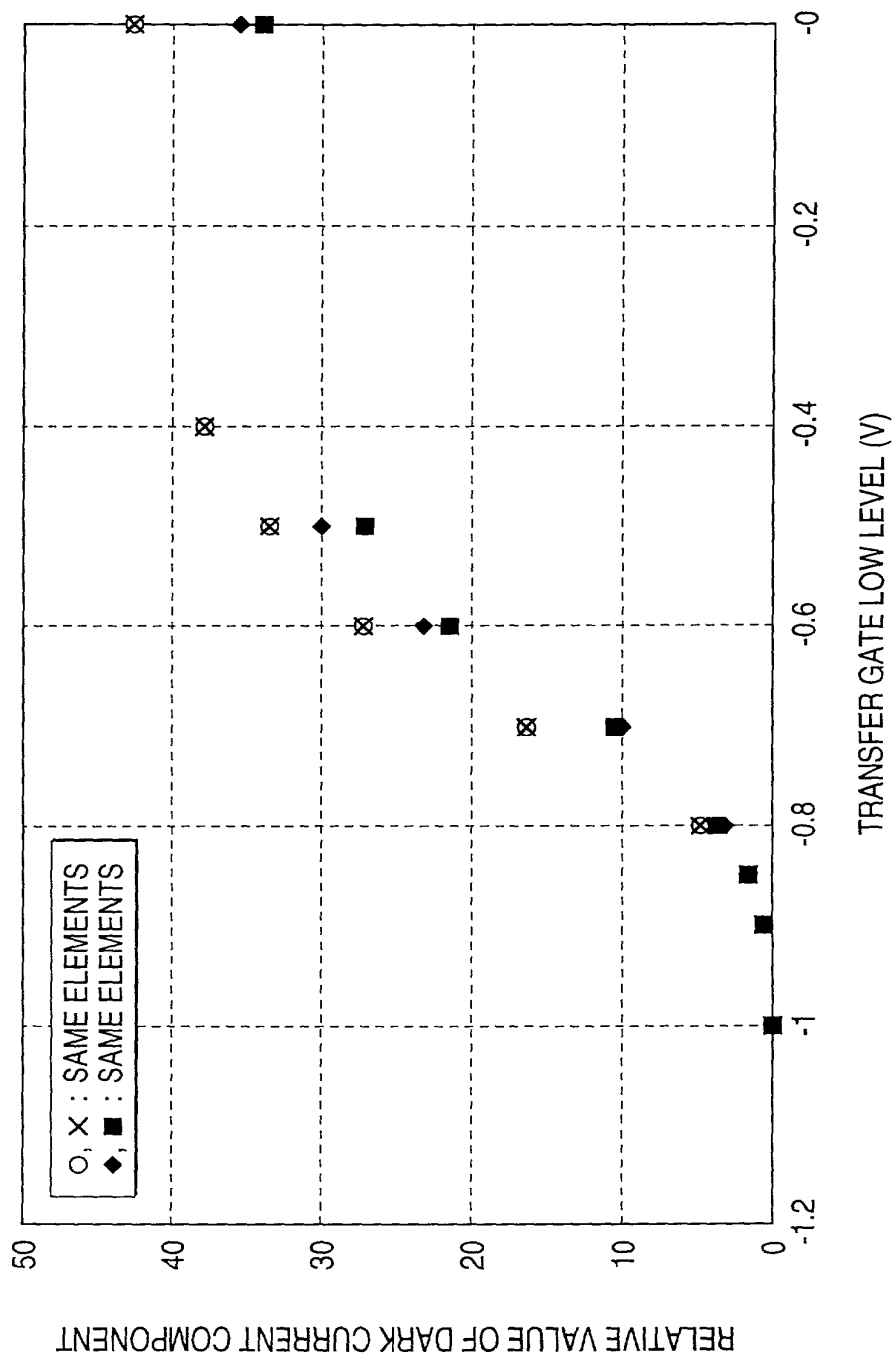


FIG. 10

DIAGRAM OF DEVICE OF DARK CURRENT

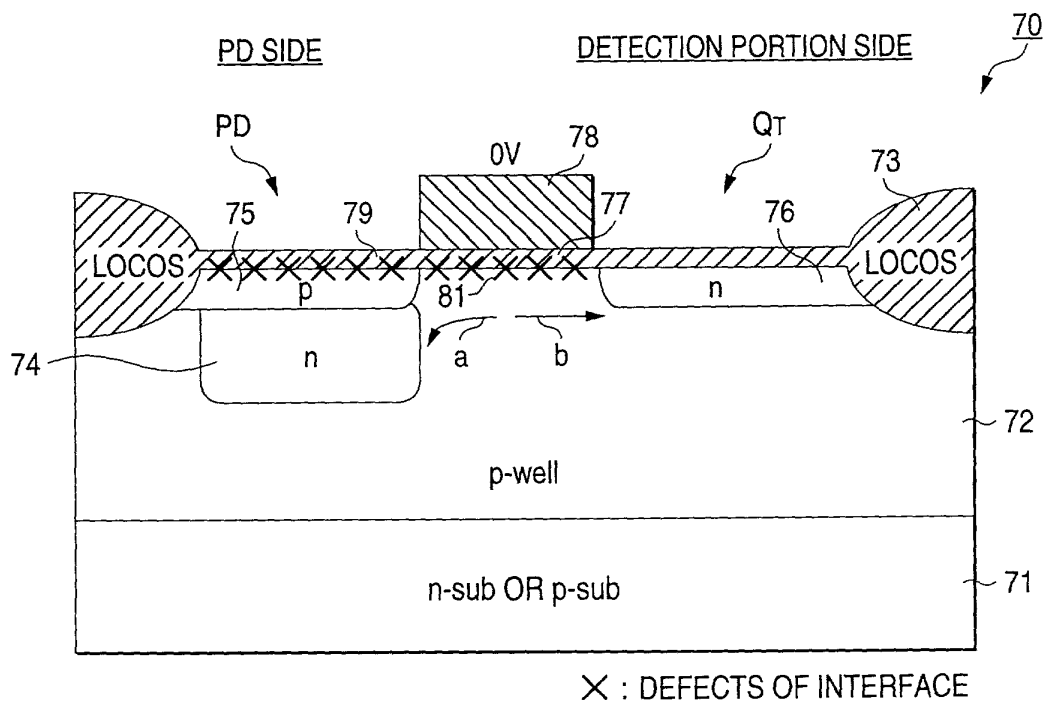


FIG. 11

CROSS-SECTIONAL VIEW OF PRIOR ART

